| L<br>Number | Hits | Search Text  | DB  | Time stamp          |
|-------------|------|--|---|---------------------|
| 11          | 0    | 6225699.URPN.  | USPAT                                       | 2002/08/16<br>10:30 |
| 12          | 13   | ("4703483"   "5109320"   "5323060"  <br>"5399898"   "5401672"   "5434453"  <br>"5446247"   "5495394"   "5541449"  <br>"5563773"   "5576519"   "5600541"  <br>"5760478").PN.  | USPAT                                       | 2002/08/16 10:31    |
| 13          | 28   | 5760478.URPN.  | USPAT                                       | 2002/08/16<br>10:32 |
| 16          | 0    | 6291267.URPN.  | USPAT                                       | 2002/08/16          |
| 17          | 8    | ("4326214"   "5307240"   "5328559"  <br>"5383269"   "5616958"   "5760478"  <br>"5963429"   "6084308").PN.  | USPAT                                       | 2002/08/16<br>10:35 |
| 18          | 17   | ("5198963"   "5323060"   "5502289"   "5608262"   "5614766"   "5715144"   "5760478"   "5790384"   "5804004"   "5808878"   "5869894"   "5926379"   "5939782"   "5949135"   "5977640"   "6051886"   "6084308").PN.  | USPAT                                       | 2002/08/16          |
| 19          | 29   | 5790384.URPN.  | USPAT                                       | 2002/08/16<br>10:39 |
| 20          | 23   | ("5072364"   "5142634"   "5230068"   "5287467"   "5303356"   "5394529"   "5428786"   "5454117"   "5469551"   "5508556"   "5564118"   "5592634"   "5623614"   "5630157"   "5732278"   "5760478"   "5834835"   "5856937"   "5903908"   "5939782"   "5977640"   "5994166"   "6031284").PN.  | USPAT                                       | 2002/08/16 10:43    |
| 21          | 12   | 6150724.URPN.  | USPAT                                       | 2002/08/16          |
| 22          | 0    | 6339254.URPN.  | USPAT                                       | 2002/08/16          |
| 23          | 5    | ("5977640"   "6127726"   "6137164"  <br>"6147401"   "6150724").PN.   | USPAT                                       | 2002/08/16          |
| 64          | 9898 | chip adj1 chip   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16 10:52    |
| 65          | 5010 | (chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16 14:47    |
| 66          | 3515 | <pre>((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))</pre>   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16          |
| 67          | 197  | (((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16 13:14    |
| 68          | 16   |  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16          |
| 69          | 181  | ((((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) not ((((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and ((second adj (chip die)) with (lager smaller))) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16<br>13:14 |

|    |     | /YTO COAC11A A TTG COCEDE1 A  | TIGDAG                                      | T 0000 100 125      |
|----|-----|---|---|---------------------|
| 70 | 26  | (US-6246114-\$ or US-6265771-\$ or US-6208027-\$ or US-6218202-\$ or US-6225699-\$ or US-6184062-\$ or US-6150186-\$ or US-6133637-\$ or US-6037662-\$ or US-5973696-\$ or US-5977640-\$ or US-5930599-\$ or US-6369448-\$ or US-6294406-\$ or US-6291267-\$ or US-6084308-\$ or US-6215193-\$ or US-6219254-\$ or US-6790384-\$ or US-6219254-\$ or US-6204562-\$ or US-6339254-\$ or US-6150724-\$ or US-6339254-\$ or  | USPAT                                       | 2002/08/16 13:14    |
| 71 | 180 | US-6147401-\$ or US-6239484-\$).did.  (((((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) not (((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and ((second adj (chip die)) with (lager smaller)))) not ((US-6246114-\$ or US-6265771-\$ or US-6208027-\$ or US-6218202-\$ or US-6184062-\$ or US-6150186-\$ or US-6133637-\$ or US-637662-\$ or US-5773896-\$ or US-6369448-\$ or US-5930599-\$ or US-6369448-\$ or US-6294406-\$ or US-6291267-\$ or US-6084308-\$ or US-6215193-\$ or US-5808878-\$ or US-6204562-\$ or US-5760478-\$ or US-6204562-\$ or US-5760478-\$ or US-6150724-\$ or US-6339254-\$ or | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16 13:25    |
| 72 | 2   | US-6147401-\$ or US-6239484-\$).did.) "6225699"   | USPAT;<br>US-PGPUB;<br>EPO; JPO;            | 2002/08/16          |
| 73 | 3   | "6333564"   | DERWENT<br>USPAT;<br>US-PGPUB;<br>EPO; JPO; | 2002/08/16 13:25    |
| 74 | 1   | "6414396"   | DERWENT USPAT; US-PGPUB; EPO; JPO;          | 2002/08/16<br>13:26 |
| 75 | 1   | "6400007"   | DERWENT USPAT; US-PGPUB; EPO; JPO; DERWENT  | 2002/08/16<br>13:26 |
| 76 | 7   | "6225699" "6333564" "6414396" "6400007"   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16 13:34    |
| 77 | 0   | 6400007.URPN.   | USPAT                                       | 2002/08/16          |
| 78 | 7   | ("5804874"   "RE36613"   "6087718"  <br>"6100594"   "6180881"   "6271598"  <br>"6316727").PN.   | USPAT                                       | 2002/08/16<br>13:27 |
| 79 | 5   | 6100594.URPN.   | USPAT                                       | 2002/08/16          |
| 80 | 7   | ("5804874"   "RE36613"   "6087718"  <br>"6100594"   "6180881"   "6271598"  <br>"6316727").PN.   | USPAT                                       | 2002/08/16          |
| 81 | 0   | 6333564.URPN.   | USPAT                                       | 2002/08/16<br>13:33 |
| 82 | 3   | ("5384698"   "5521435"   "6087203").PN.   | USPAT                                       | 2002/08/16<br>13:33 |
| 83 | 0   | 6225699.URPN.   | USPAT                                       | 2002/08/16<br>13:33 |
|    | 1   |   | L   | <u></u>             |

| 84 | 3318 | (((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) not (((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller)))  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16<br>13:34                   |
|----|------|---|---|---------------------------------------|
| 85 | 150  | ((((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) not (((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) and ((second adj (chip die)) and (lager smaller)) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16 13:47                      |
| 86 | 150  |   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16<br>13:37                   |
| 87 | 2674 | ((second adj (chip die)) and (lager smaller))   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16                            |
| 88 | 233  | ((second adj (chip die)) with (lager smaller))  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16                            |
| 89 | 217  | <pre>(((second adj (chip die)) with (lager<br/>smaller))) not (((chip adj1 chip) and<br/>(bump ball flipchip (flip adj chip) wire<br/>wiring)) and (substrate (printed adj<br/>circuit adj board))) and (second with<br/>(lager smaller)))</pre>  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16                            |
| 90 | 40   | <pre>((((second adj (chip die)) with (lager<br/>smaller))) not ((((chip adj1 chip) and<br/>(bump ball flipchip (flip adj chip) wire<br/>wiring)) and (substrate (printed adj<br/>circuit adj board))) and (second with<br/>(lager smaller))) and (bump ball<br/>flipchip (flip adj chip))</pre>   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16 13:51                      |
| 91 | 0    | 20020045290.URPN.   | USPAT                                       | 2002/08/16                            |
| 92 | 18   | ("5012323"   "5128831"   "5291061"   "5322207"   "5323060"   "5387815"   "5399898"   "5422435"   "5426566"   "5434745"   "5483024"   "5600175"   "5721452"   "5874781"   "5886412"   "5963794"   "6051886"   "6215193").PN.   | USPAT                                       | 2002/08/16 13:54                      |
| 93 | 2    | 6215193.URPN.   | USPAT                                       | 2002/08/16<br>13:55                   |
| 94 | 0    | 6337226.URPN.   | USPAT                                       | 2002/08/16<br>13:56                   |
| 95 | 2    | ("6215193"   "6238949").PN.   | USPAT                                       | 2002/08/16                            |
| 96 | 3    | ("5663106"   "6069025"   "6133637").PN.<br>("5663106"   "6069025"   "6133637").PN.  | USPAT<br>USPAT                              | 2002/08/16<br>13:56<br>2002/08/16     |
| 98 | 0    | ("5663106"   "6069025"   "613363/").PN.   | USPAT                                       | 2002/08/16<br>  13:57<br>  2002/08/16 |
|    |      | 033/220.ORFN.   | OSPAI                                       | 13:57                                 |

|     |     |  |   | ,                            |
|-----|-----|--|---|------------------------------|
| 99  | 17  | ("5198963"   "5323060"   "5502289"   "5608262"   "5614766"   "5715144"   "5760478"   "5790384"   "5804004"   "5808878"   "5869894"   "5926379"   "5939782"   "5949135"   "5977640"   "6051886"   "6084308").PN.  | USPAT                                       | 2002/08/16<br>13:57          |
| 100 | 17  | ("5198963"   "5323060"   "5502289"   "5608262"   "5614766"   "5715144"   "5760478"   "5790384"   "5804004"   "5808878"   "5869894"   "5926379"   "5939782"   "5949135"   "5977640"   "6051886"   "6084308").PN.  | USPAT                                       | 2002/08/16<br>13:59          |
| 101 | 3   | 5963794.URPN.  | USPAT                                       | 2002/08/16<br>14:00          |
| 102 | 8   | ("4996587"   "5012323"   "5168345"  <br>"5291061"   "5313693"   "5323060"  <br>"5422435"   "5789805").PN.  | USPAT                                       | 2002/08/16<br>14:04          |
| 103 | 10  | 5721452.URPN.  | USPAT                                       | 2002/08/16<br>14:06          |
| 104 | 2   | 5959845.URPN.  | USPAT                                       | 2002/08/16                   |
| 105 | 13  | ("3716761"   "4613924"   "5237131"   "5258891"   "5323060"   "5490040"   "5515241"   "5541814"   "5557505"   "5721452"   "5751557"   "5754408"   "5801927").PN.  | USPAT                                       | 2002/08/16<br>14:29          |
| 107 | 0   | 6365963.URPN.  | USPAT                                       | 2002/08/16                   |
| 108 | 3   | ("5808878"   "5869894"   "6181008").PN.  | USPAT                                       | 14:32<br>2002/08/16<br>14:32 |
| 109 | 3   | 6181008.URPN.  | USPAT                                       | 2002/08/16<br>14:34          |
| 110 | 9   | ("5608262"   "5635767"   "5646828"  <br>"5808878"   "5869894"   "6150724"  <br>"6181008"   "6201302"   "6204562").PN.  | USPAT                                       | 2002/08/16<br>14:35          |
| 111 | 177 | ((((second adj (chip die)) with (lager smaller))) not (((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) not ((((second adj (chip die)) with (lager smaller))) not (((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) and (bump ball flipchip (flip adj chip)))  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16<br>14:40          |
| 112 | 69  | (((((second adj (chip die)) with (lager smaller))) not ((((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) not ((((second adj (chip die)) with (lager smaller))) not (((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) and (bump ball flipchip (flip adj chip))) and (substrate base (printed adj circuit adj board))) and (substrate base (printed adj circuit adj board)) | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16<br>14:46          |
| 113 | 362 | (((bottom lower) adj (chip die)) with (lager smaller))   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16<br>14:46          |

| 100 | 1 / / / / / / / / / / / / / / / / / / /   |  | 10000 (00 (7 5   |
|-----|---|--|--|
| 108 | <pre>(((((second adj (chip die)) with (lager<br/>smaller))) not (((chip adj1 chip) and<br/>(bump ball flipchip (flip adj chip) wire<br/>wiring)) and (substrate (printed adj<br/>circuit adj board))) and (second with<br/>(lager smaller))) not ((((second adj<br/>(chip die)) with (lager smaller))) not<br/>(((chip adj1 chip) and (bump ball<br/>flipchip (flip adj chip) wire wiring))<br/>and (substrate (printed adj circuit adj<br/>board))) and (second with (lager<br/>smaller)))) and (bump ball flipchip (flip<br/>adj chip))) not (((((second adj (chip<br/>die)) with (lager smaller))) not ((((chip<br/>adj1 chip) and (bump ball flipchip (flip<br/>adj chip) wire wiring)) and (substrate<br/>(printed adj circuit adj board))) and<br/>(second with (lager smaller))) not<br/>((((second adj (chip die)) with (lager<br/>smaller))) not (((chip adj1 chip) and<br/>(bump ball flipchip (flip adj chip) wire<br/>wiring)) and (substrate (printed adj<br/>circuit adj board))) and (second with<br/>(lager smaller))) and (bump ball<br/>flipchip (flip adj chip)))) and<br/>(substrate base (printed adj circuit adj<br/>board)))</pre>   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT  | 2002/08/16 14:47   |
| 103 | (((bottom lower) adj (chip die)) with (lager smaller))) and (bump ball flipchip (flip adj chip) wire wiring)  | USPAT;<br>US-PGPUB;<br>EPO; JPO;   | 2002/08/16<br>14:48  |
| 24  | <pre>(((((((second adj (chip die)) with (lager<br/>smaller))) not (((chip adj1 chip) and<br/>(bump ball flipchip (flip adj chip) wire<br/>wiring)) and (substrate (printed adj<br/>circuit adj board))) and (second with<br/>(lager smaller))) not ((((second adj<br/>(chip die)) with (lager smaller))) not<br/>(((chip adj1 chip) and (bump ball<br/>flipchip (flip adj chip) wire wiring))<br/>and (substrate (printed adj circuit adj<br/>board))) and (second with (lager<br/>smaller))) and (bump ball flipchip (flip<br/>adj chip))) not (((((second adj (chip<br/>die)) with (lager smaller))) not ((((chip<br/>adj1 chip) and (bump ball flipchip (flip<br/>adj chip) wire wiring)) and (substrate<br/>(printed adj circuit adj board))) and<br/>(second with (lager smaller)))) not<br/>((((second adj (chip die)) with (lager<br/>smaller))) not (((chip adj1 chip) and<br/>(bump ball flipchip (flip adj chip) wire<br/>wiring)) and (substrate (printed adj<br/>circuit adj board))) and (second with<br/>(lager smaller)))) and (bump ball<br/>flipchip (flip adj chip))) and<br/>(substrate base (printed adj circuit adj<br/>board)))) and (bump ball flipchip (flip<br/>adj chip) wire wiring)</pre> | DERWENT USPAT; US-PGPUB; EPO; JPO; DERWENT   | 2002/08/16 14:48   |
|     | 103   | <pre>smaller)) not ((((chip adj1 chip) and   (bump ball flipchip (flip adj chip) wire   wiring)) and (substrate (printed adj   circuit adj board)) and (second with   (lager smaller))) not ((((second adj   (chip die)) with (lager smaller))) not   ((((chip adj1 chip) and (bump ball   flipchip (flip adj chip) wire wiring))   and (substrate (printed adj circuit adj   board))) and (second with (lager   smaller))) and (bump ball flipchip (flip   adj chip))) not (((((second adj (chip   die)) with (lager smaller))) not ((((chip   adj1 chip) and (bump ball flipchip (flip   adj chip) wire wiring)) and (substrate   (printed adj circuit adj board))) and   (second with (lager smaller))) not   ((((second adj (chip die)) with (lager   smaller))) not ((((chip adj1 chip) wire   wiring)) and (substrate (printed adj   circuit adj board))) and (second with   (lager smaller)))) and (bump ball   flipchip (flip adj chip)))) and   (substrate base (printed adj circuit adj   board)))   ((((bottom lower) adj (chip die)) with   (lager smaller))) and (bump ball flipchip   (flip adj chip) wire wiring)  24   (((((second adj (chip die)) with (lager   smaller))) not ((((chip adj1 chip) and   (bump ball flipchip (flip adj chip) wire   wiring)) and (substrate (printed adj   circuit adj board))) and (second with   (lager smaller))) not ((((second adj   (chip die)) with (lager smaller))) not   ((((second adj (chip die)) with (lager   smaller)))) not ((((second adj   (chip die)) with (lager smaller))) not   ((((chip adj1 chip) and (bump ball   flipchip (flip adj chip) wire wiring))   and (substrate (printed adj   circuit adj board))) and (substrate   (printed adj circuit adj board))) and   (substrate dadj circuit adj board))) and   (substrate dadj circuit adj board))) and   (sucscond adj (chip die)) with (lager   smaller)))) not ((((chip adj1 chip) and   (bump ball flipchip (flip adj chip) wire   wiring)) and (substrate (printed adj   circuit adj board))) and (bump ball   flipchip (flip adj chip)) wire   wiring)) and (substrate (printed adj   circu</pre> | <pre>smaller))) not ((((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) not (((((second adj (chip die)) with (lager smaller))) not (((((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller)))) and (bump ball flipchip (flip adj chip))) not (((((second adj (chip die)) with (lager smaller))) not ((((chip adj1 chip))) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) not (((((second adj (chip die)) with (lager smaller))) not ((((chip adj1 chip) and (bump ball flipchip (flip adj chip))) and (substrate base (printed adj circuit adj board))) ((((second adj (chip die)) with (lager smaller))) and (bump ball flipchip (flip adj chip) wire wiring)  24 (((((second adj (chip die)) with (lager smaller))) not ((((chip adj1 chip) and (bump ball flipchip (flip adj chip)) ire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) not ((((second adj (chip die)) with (lager smaller))) not ((((chip adj1 chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (substrate (printed adj circuit adj board))) and (second with (lager smaller)))) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) not ((((second adj (chip die)) with (lager smaller)))) and (bump ball flipchip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller)))) and (bump ball flipchip (flip adj chip)) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller)))) and (bump ball flipchip (flip adj chip)) wire wiring)) and (substrate (printed adj circuit adj board)))) and (second with (lager s</pre> |

| 117 | 107   | [////bottom lovem) and /article distribution   | IICDAM -                                    | 2002/00/16                                 |
|-----|-------|--|---|--|
|     | 127   | (lager smaller)) and (bump ball flipchip (flip adj chip) wire wiring)) (((((((second adj (chip die)) with (lager smaller))) not (((chip adjl chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) not ((((second adj (chip die)) with (lager smaller))) not ((((chip adjl chip) and (bump ball flipchip (flip adj chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller))) and (bump ball flipchip (flip adj chip))) not ((((second adj (chip die)) with (lager smaller))) not ((((chip adjl chip))) and (bump ball flipchip (flip adj chip)) and (bump ball flipchip (flip adj chip) and (bump ball flipchip (flip adjl chip) and (bump ball flipchip (flip adjl chip) wire wiring)) and (substrate (printed adj circuit adj board))) and (second with (lager smaller)))) not | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16 14:48                           |
| 118 | 3     | <pre>(((((second adj (chip die)) with (lager<br/>smaller))) not ((((chip adj1 chip) and<br/>(bump ball flipchip (flip adj chip) wire<br/>wiring)) and (substrate (printed adj<br/>circuit adj board))) and (second with<br/>(lager smaller))) and (bump ball<br/>flipchip (flip adj chip))) and<br/>(substrate base (printed adj circuit adj<br/>board))) and (bump ball flipchip (flip<br/>adj chip) wire wiring))<br/>("5252179"   "60333994"   "6093331").PN.</pre>   | USPAT                                       | 2002/08/16                                 |
| 119 | 9     | (chip adj1 chip) and (spacer near ring)  | USPAT;<br>USPAT;<br>US-PGPUB;<br>EPO; JPO;  | 2002/08/16<br>14:54<br>2002/08/16<br>15:37 |
| 120 | 10416 | spacer near ring   | DERWENT USPAT; US-PGPUB; EPO; JPO;          | 2002/08/16<br>15:10                        |
| 121 | 1     | (spacer near ring) with substrate with chip  | DERWENT USPAT; US-PGPUB; EPO; JPO; DERWENT  | 2002/08/16<br>15:12                        |
| 122 | 44823 | support\$3 near ring   | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16<br>15:12                        |
| 123 | 42    | (support\$3 near ring) with substrate with chip  | USPAT;<br>US-PGPUB;<br>EPO; JPO;<br>DERWENT | 2002/08/16<br>15:13                        |
| 124 | 42    | (support\$3 near ring) with ((pcb substrate) with chip)  | USPAT;<br>US-PGPUB;<br>EPO; JPO;            | 2002/08/16<br>15:24                        |
| 125 | 3     | "6333564"  | DERWENT USPAT; US-PGPUB; EPO; JPO;          | 2002/08/16<br>15:27                        |
| 126 | 2932  | (chip adj1 chip) and (ring surround surrounding)   | DERWENT USPAT; US-PGPUB; EPO; JPO;          | 2002/08/16<br>15:38                        |
| 127 | 43    | (chip adj1 chip) and (spacer with (ring surround surrounding))   | DERWENT USPAT; US-PGPUB; EPO; JPO; DERWENT  | 2002/08/16<br>15:39                        |
| 128 | 0     | 20020096785.URPN.  | USPAT                                       | 2002/08/16<br>15:41                        |

| 129 | 37   ("4954878"   "5099306"   "5109320"   | USPAT | 2002/08/16 |
|-----|---|-------|------------|
|     | "5128831"   "5222014"   "5239198"         |       | 15:46      |
|     | "5252857"   "5291061"   "5300801"         |       |            |
|     | "5323060"   "5403784"   "5422435"         |       |            |
|     | "5434745"   "5466627"   "5473814"         |       |            |
|     | "5477082"   "5481134"   "5494841"         |       |            |
|     | "5495398"   "5496775"   "5498902"         |       |            |
|     | "5498905"   "5506756"   "5508565"         |       |            |
|     | "5512765"   "5512780"   "5513076"         |       |            |
|     | "5535101"   "5594275"   "5612576"         |       |            |
|     | "5639696"   "5705858"   <b>"</b> 5715144" |       |            |
|     | "5728606"   "5729440"   "6051878"         |       |            |
|     | "6222265").PN.                            |       |            |